

F-2  
-  
50  
-  
F-1

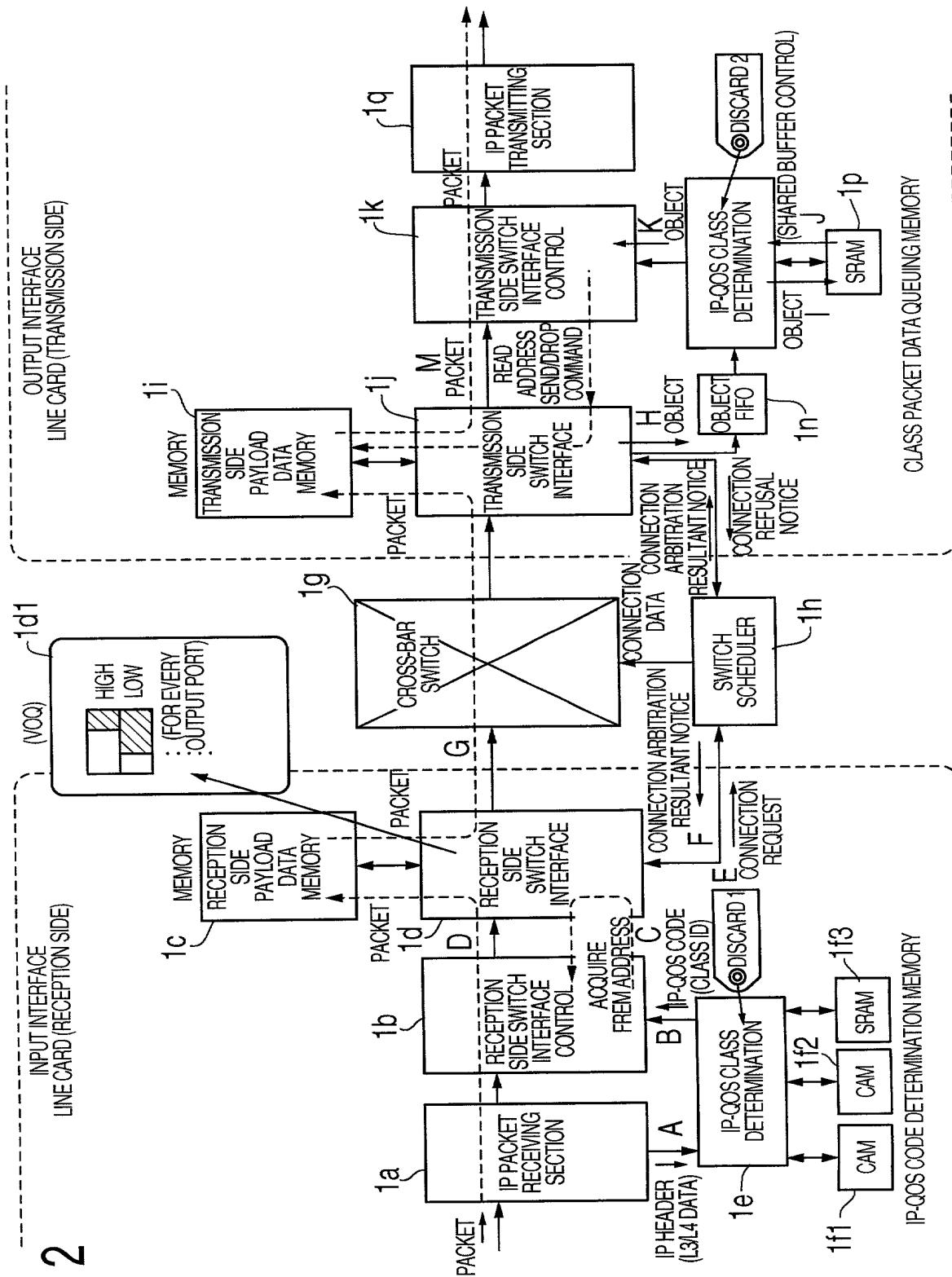


Fig. 3

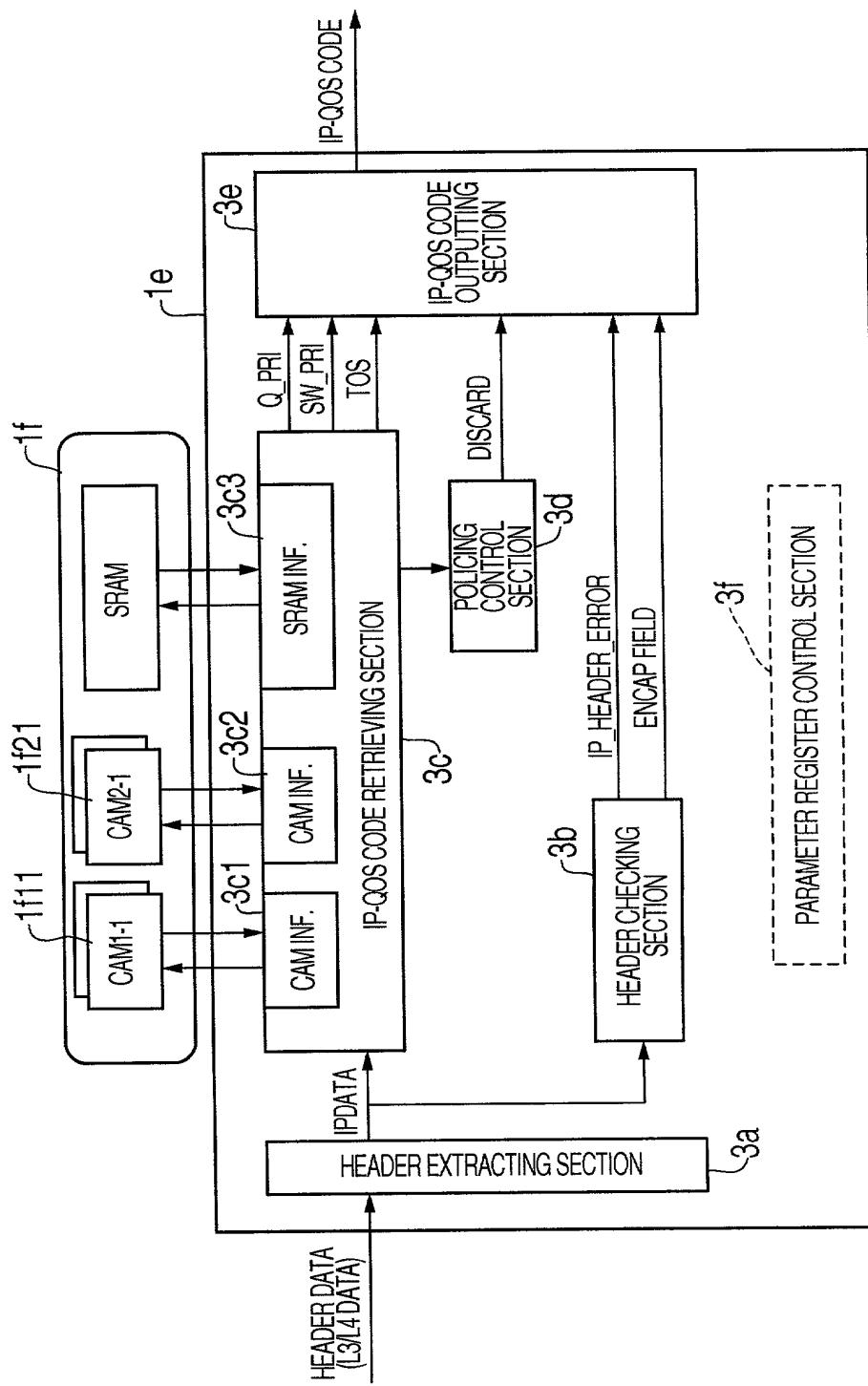


Fig. 4

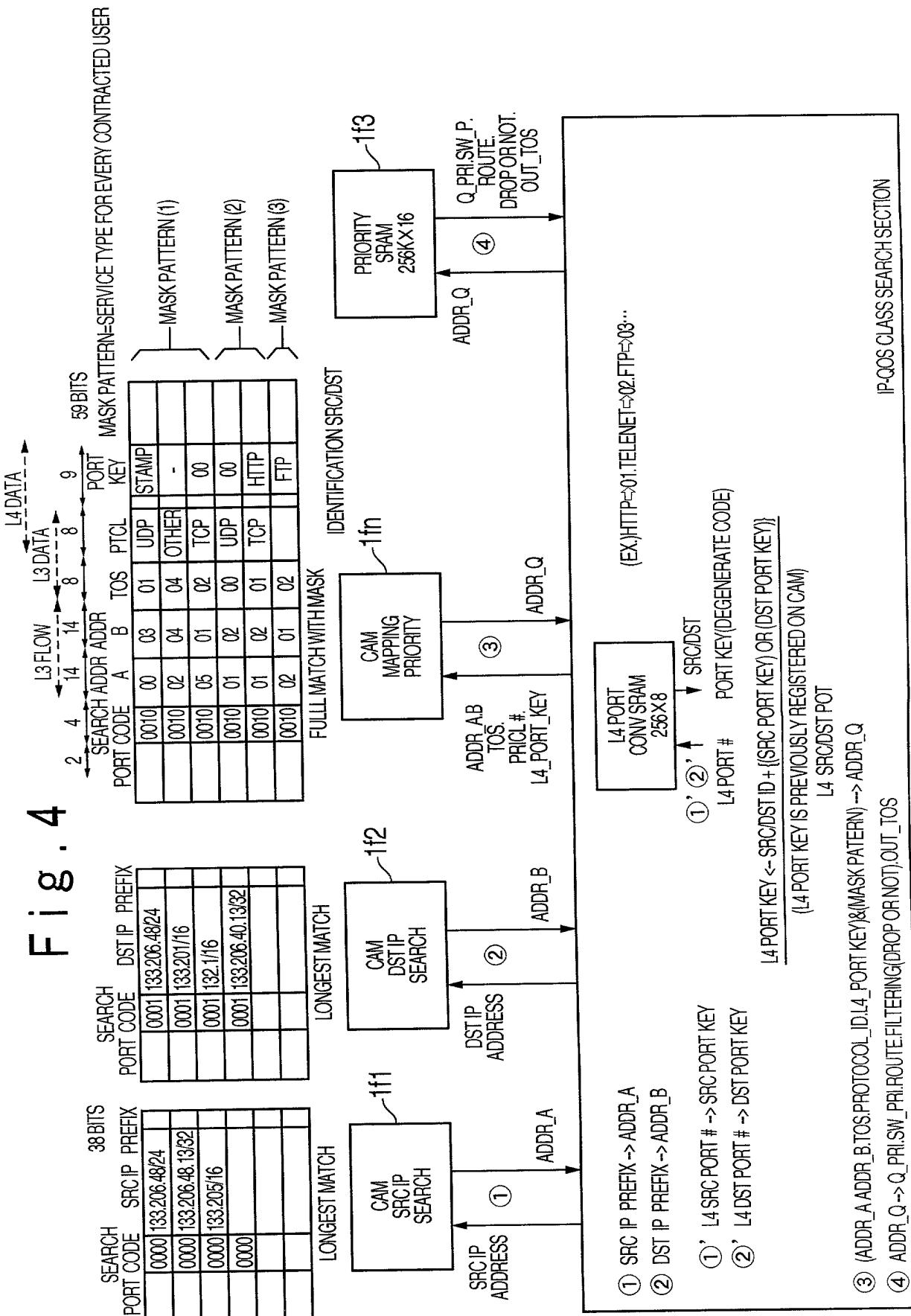


Fig. 5

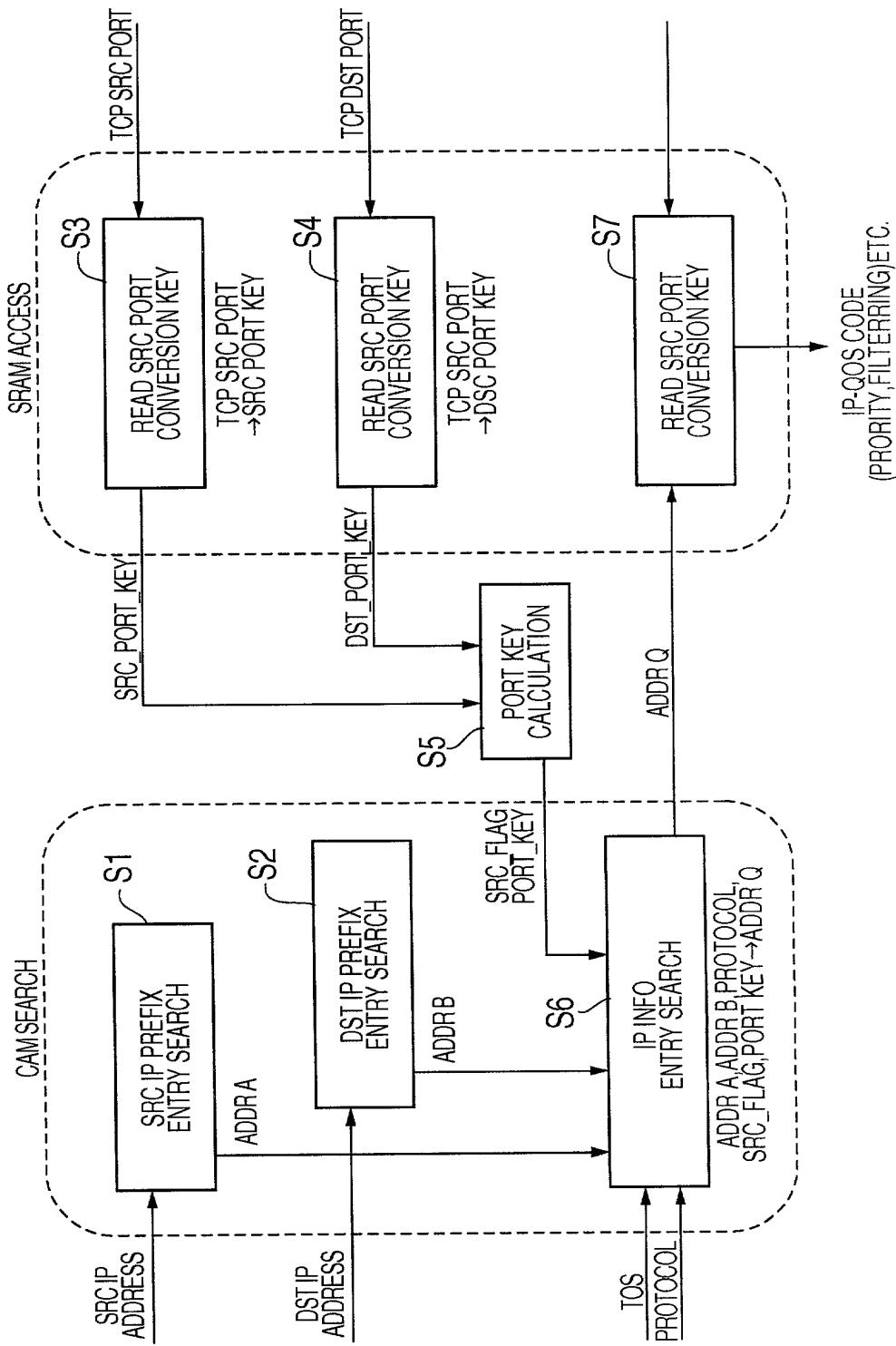


Fig. 6 A

[CAM REGION DIVISION]

CAM ADDRESS	CAM DATA (MAX.64 BITS)	MASK PATTERN (64 BITS)	SEARCH METHOD
ADDR_A~	IP SRC PREFIX ENTRY STORAGE REGION		LONGEST MATCH
ADDR_B~	IP DST PREFIX ENTRY STORAGE REGION		LONGEST MATCH
ADDR_Q~	IP INFO SEARCH ENTRY STORAGE REGION		FULL MATCH WITH MASK

Fig. 6 B

[1,IP SRC PREFIX ENTRY STORAGE REGION : SEARCH CODE 0000]

CAM ADDRESS (ADDR_A)	CAM DATA (38 BITS)			
	HW #2)	SEARCH CODE(4)	IP SRC ADDRESS/ PREFIX(32BITS)	NON USED(26 BITS)
A #1	00	0000	IP SRC ADDRESS #1/PREFIX	
A #2	00	0000	IP SRC ADDRESS #2/PREFIX	
A #3	01	0000	IP SRC ADDRESS #1/PREFIX	
:	:	:	:	

Fig. 7 A

【2,IP DST PREFIX ENTRY STORAGE REGION : SEARCH CODE 0001】

CAM ADDRESS (ADDR_B)	CAM DATA (38 BITS)			
	HW #(2)	SEARCH CODE(4)	IP DST ADDRESS/ PREFIX(32BITS)	NON USED(26 BITS)
B #1	00	0001	IP DST ADDRESS #1/PREFIX	
B #2	00	0001	IP DST ADDRESS #2/PREFIX	
B #3	01	0001	IP DST ADDRESS #1/PREFIX	
⋮	⋮	⋮	⋮	

Fig. 7 B

【3,IP INFO ADDRESS ENTRY STORAGE REGION : SEARCH CODE 0010】

Fig. 8

【IP INFO ENTRY】

ADDRESS(16 BITS): UPPER 2 BITS=00 LOWER 14 BITS=HIT ADDR_Q		DATA(24 BIT)				RESERVE (3)
		Q_PRI(4)	D	P	ROUTE(1+4) TOS(2+8)	
ADDR Q0		0000	0	0	0000	11 011011 00
ADDR Q1		1101	0	1	0000	11 011010 00
ADDR Q2		1101	0	0	0000	00 000000 00
⋮	⋮	⋮	⋮	⋮	⋮	⋮
ADDR Q1		1110	0	1	1 0101	00 000000 00
⋮	⋮	⋮	⋮	⋮	⋮	⋮

Fig. 9

(IPV4 & TCP/UDP/OTHER HEADER FORMAT)

WORD	63	47	31	15
-	EMPTY DATA			
0	VER	IHL	TOS	DETAGRAM LENGTH
1	TTL	PROTOC OL	HEADER CHECKSUM	SRC IP ADDRESS
2	DST IP ADDRESS			L4 SRC PORT
				L4 DST PORT

F-10

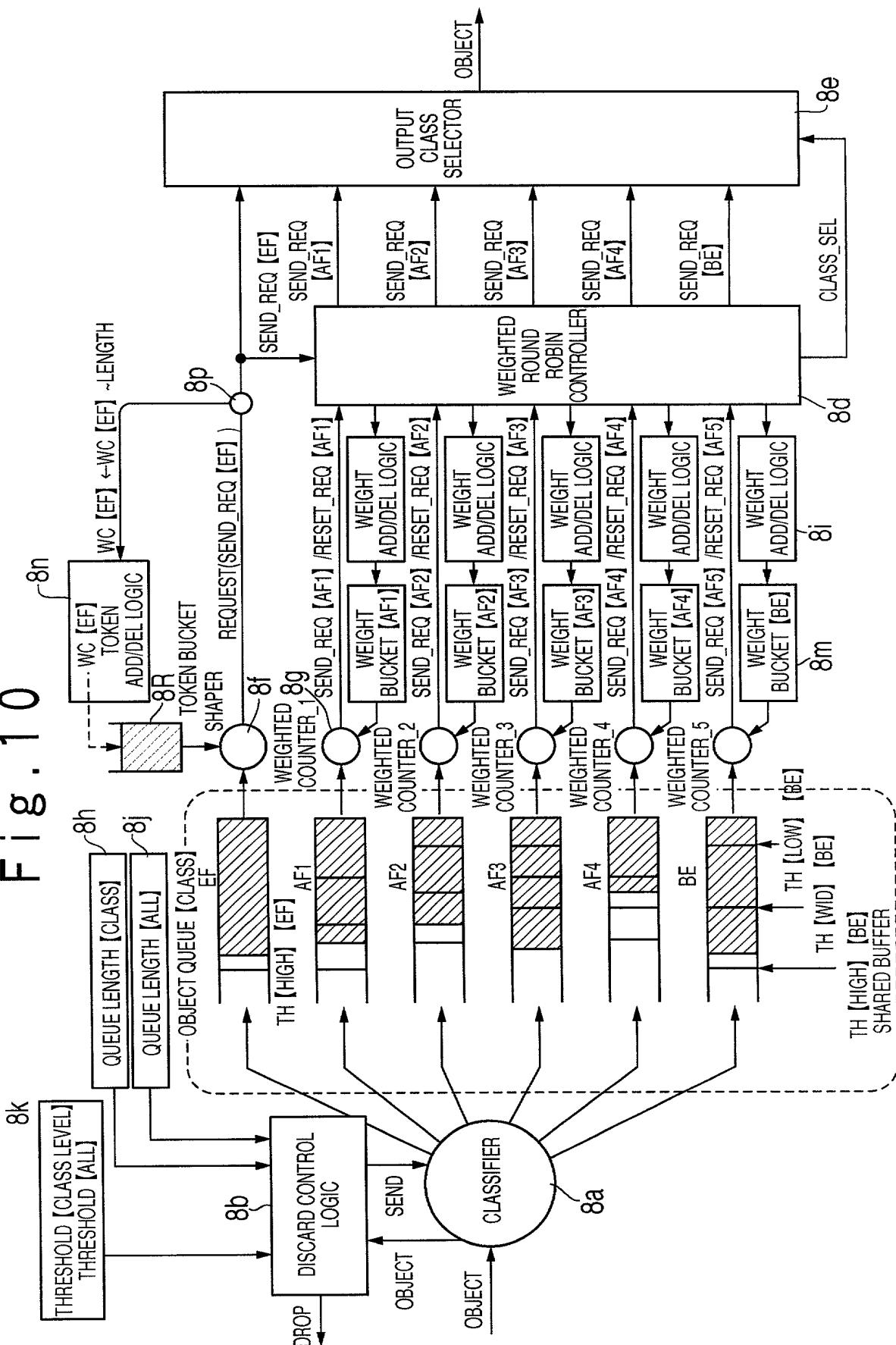


Fig. 11

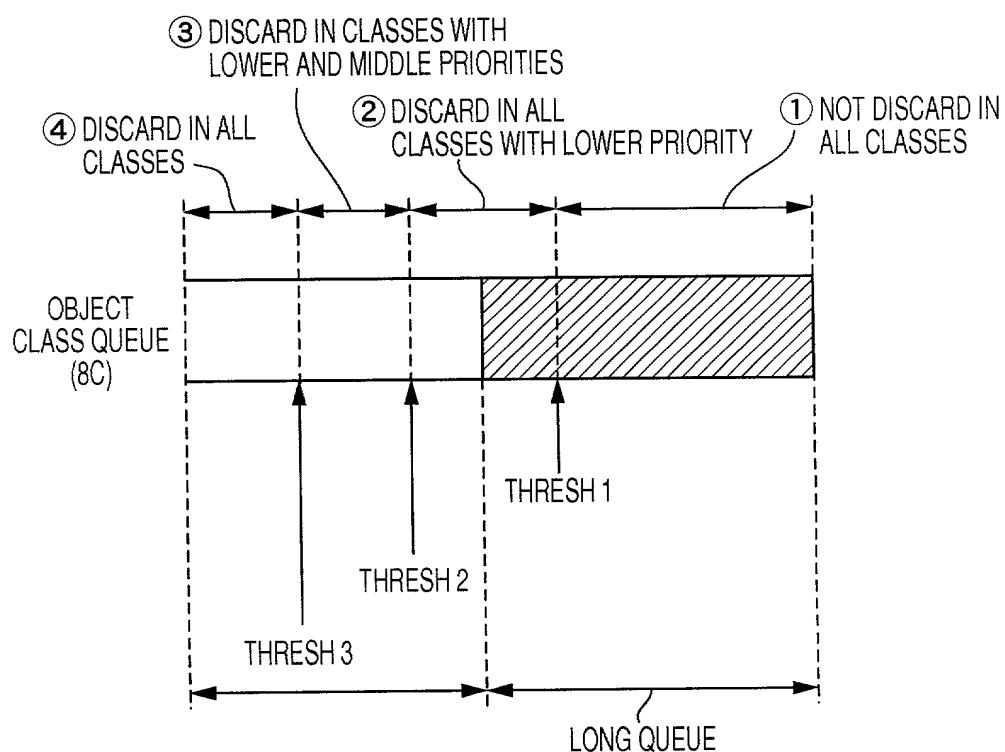


Fig. 12

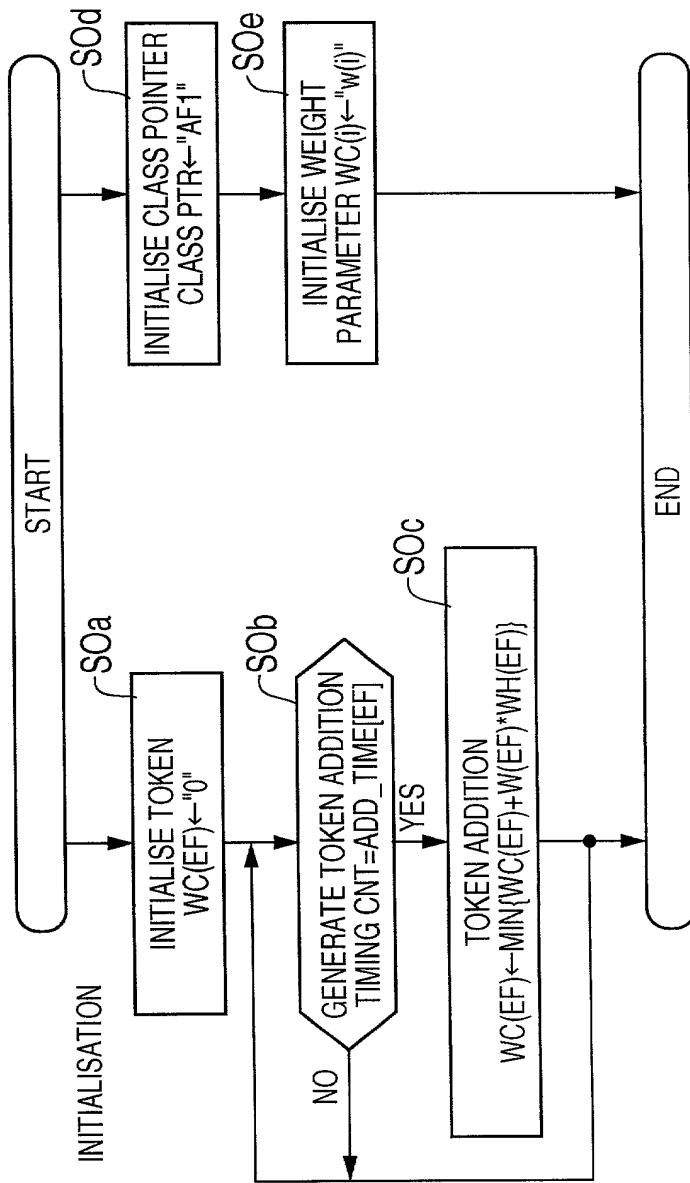


Fig. 13

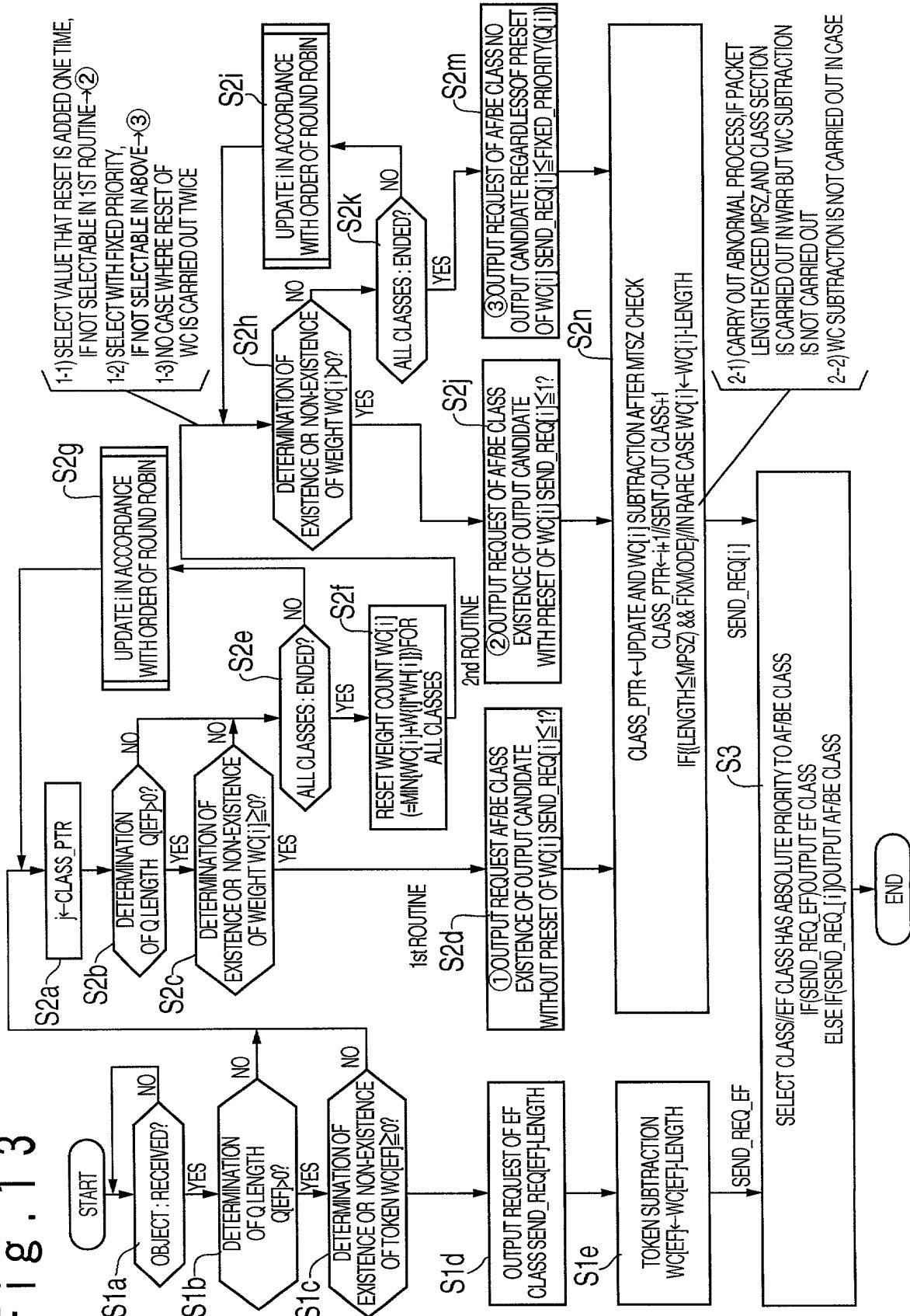
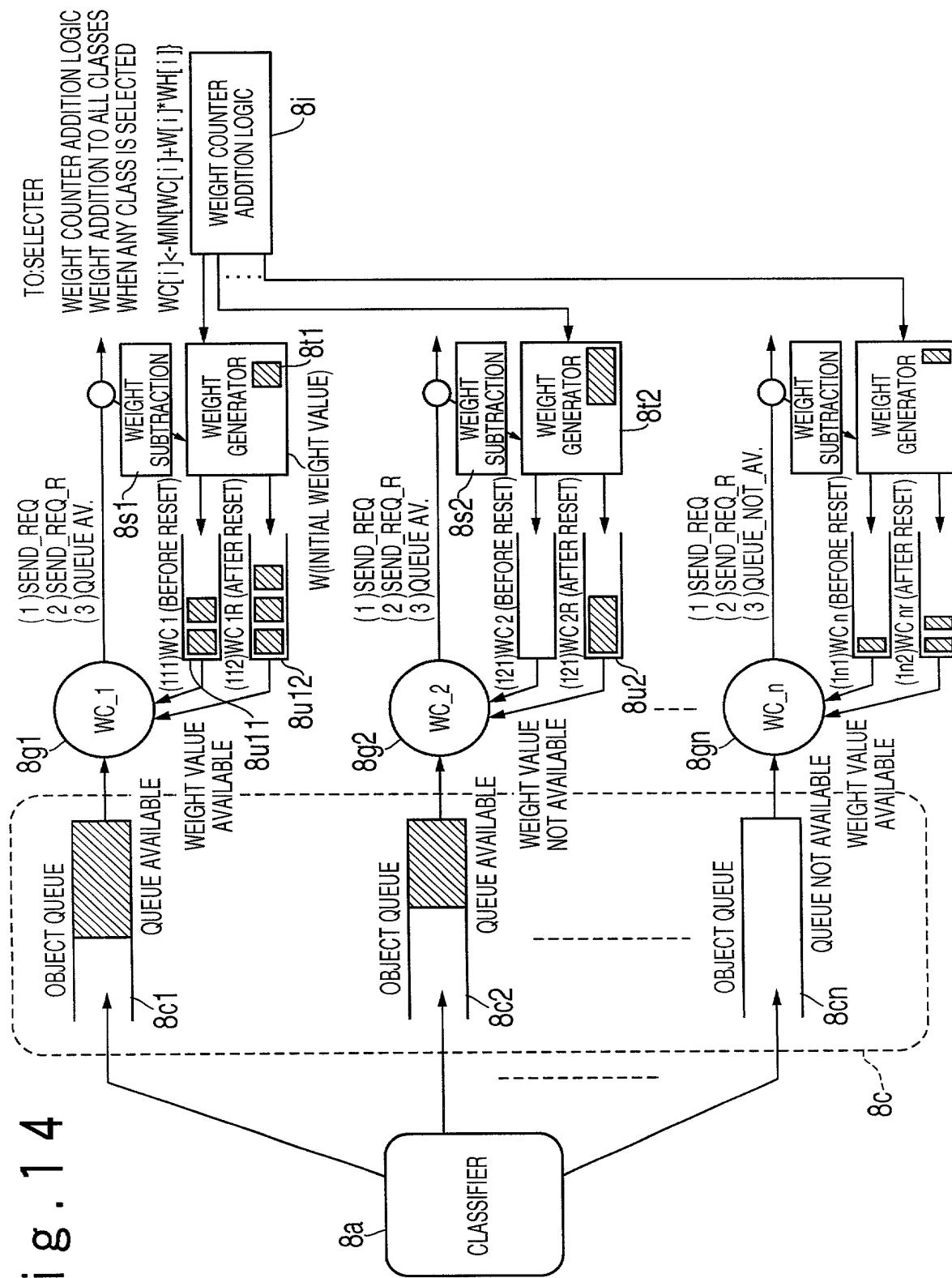


Fig. 14



15.00

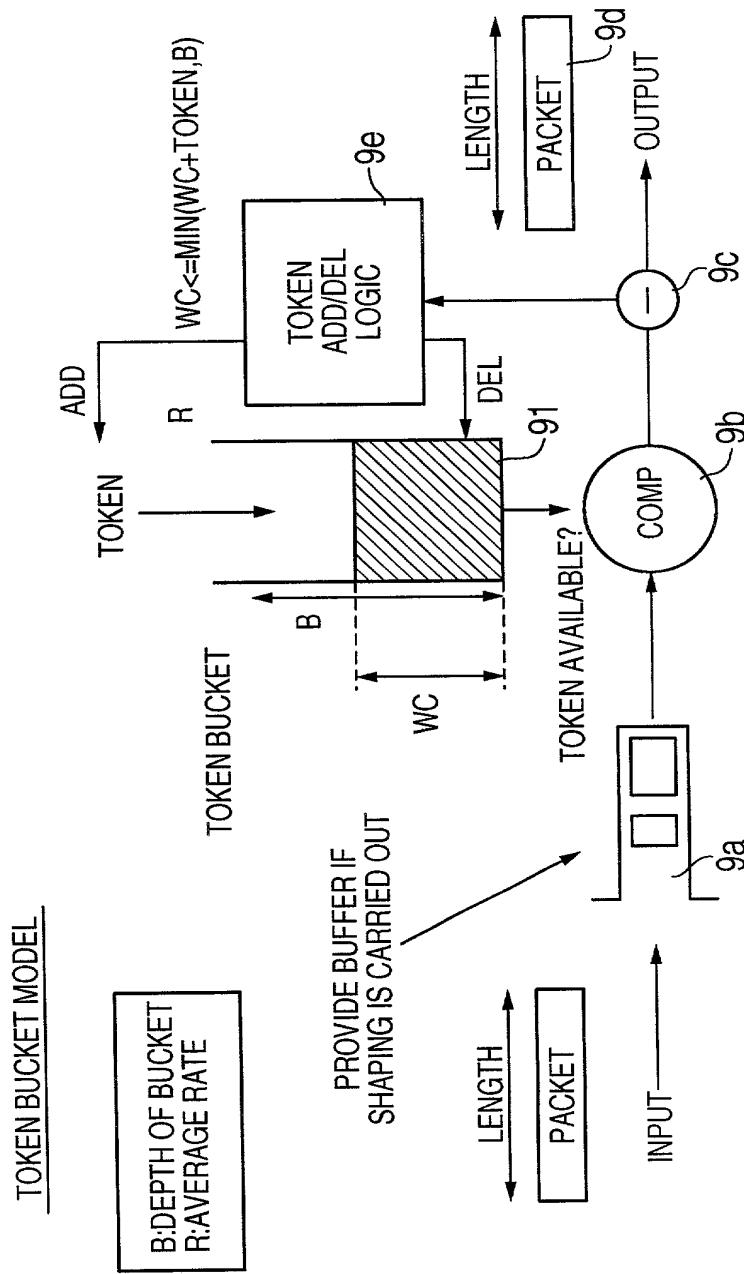


Fig. 16

